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	Application Number		10806871
	First Named Inventor Dimps Art Unit Examiner Name Savia,		2004-03-22
			sey et al.
			2185
			, Arpan P.
			AUS920040064US1

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	4	6961925		2005-11-01	Callahan, II et al.	
	5	5950003		1999-09-30	Kaneshiro et al.	
	6	5212794		1993-05-18	Pettis et al.	
	7	5659679		1997-08-19	Alpert et al.	
	8	6006033		1999-12-21	Heisch	

9	6119075		2000-09-12	Dean et al.	
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15	5794028		1998-08-11	Tran	
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17	5950003		1999-09-07	Kaneshiro et al.	
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	1	RAMIREZ et al., 'The Effect of Code Reordering on Branch Prediction', Proceedings of the International Conference on Parallel Architectures and Compilation Techniques, October 2000, pp. 180-198.	
	2	YANG et al., "Improving Performance by Branch Reordering", Proceedings of the ACM SIGPLAN 1998 Conference on Programming Language Design and Implementation, Montreal Canada, 1008, pp. 130-141.	
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	9	INTEL, "Intel IA-64 Architecture Software Developer's Manual", Revision 1.1, Vol. 4, No. 245320.002, July 2001.	
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